



1763
3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SAVAGE et al.

Serial No. **09/996,869**

Filed: **November 27, 2001**

Title: **SEMICONDUCTOR WAFER
PROCESSING SYSTEM WITH
VERTICALLY-STACKED PROCESS
CHAMBERS AND SINGLE-AXIS
DUAL-WAFER TRANSFER SYSTEM**

Examiner: Unassigned

Art Unit: 1763

San Francisco, CA 94111

Date: **June 10, 2003**

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being sent via first class mail, postage prepaid in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA
22313-1450 on June 10, 2003.

Signed: _____

Kari Baerlan

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Sir:

Please find enclosed the following documents relating to the subject patent application:

- ☒ [X] Power of Attorney by Assignee and Exclusion of Inventor/s under 37 C.F.R. 1.32 and Statement under 37 C.F.R. 3.73(b);
- ☒ [X] Return self-addressed Postcard;
- ☒ [X] Please charge any additional fees or credit any overpayment to Deposit Account No.50-2319 (Order No. A-67736-2/MSS (463035-481)).

Respectfully submitted,

By: _____

Maria S. Swiatek, Reg. No. 37,244

DORSEY & WHITNEY LLP
Four Embarcadero Center, Suite 3400
San Francisco, CA 94111-4187
Telephone: (650) 494-8700